EAST Search History

-[Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
	L1	2383	delay\$4 and (alternativ\$6 near4 transport\$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2007/04/10 14:34
	L2)	67	(schedul\$4 near5 delay\$4) and (alternativ\$6 near4 transport\$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:45
	(3)	330	(delay\$4) same (((alternativ\$6 or other\$4) near4 transport\$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:40
	L4)	. 4	"20020065698"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:44
	L5)	2	"20020065698" and delay\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:44
	(L6)	99	(schedul\$4 near5 (disruption\$4 or change\$4)) and (alternativ\$6 near4 transport\$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:46
	L7)	84	16 not 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:49
	L8	5066 reschedul\$5		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:57
	L9	124667	yu.inv.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:51

EAST Search History

-		EAST Scarci	,			
L10	4510	yu.inv. and delay\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:51
	33	yu.inv. and irregularit\$4 and delay\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:55
L12	344	yu.inv. and irregularit\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:55
L13	2729	reschedul\$5 and delay\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 14:59
L14	681	reschedul\$5 and delay\$4 and (transportation or train\$2 or flight\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:00
(15)	637	reschedul\$5 and delay\$4 and (transportation or train\$2 or flight\$4) and schedul\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:00
L16	353	reschedul\$5 and (schedul\$4 same delay\$4) and (transportation or train\$2 or flight\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:00
L17	77	(schedul\$4 same delay\$4) and (reschedul\$4 same (passenger\$4 or transportation or train\$2 or flight\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:08
L18	4736	(schedul\$4 near4 delay\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:08
L20	49311	("705").CLAS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:08

EAST Search History

\sim		LASI Searci	ii iiistoi y			
L21	349	I18 and I20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:08
L2/2	197	(schedul\$4 same transportation same delay\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:09
(L23)	46	I20 and I22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/10 15:09

DIALOG

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Yanus PY>2001
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     621, 636, 613, 813, 16, 160, 634, 148, 20
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? show files

[File 8] Ei Compendex(R) 1884-2007/Apr W1

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[File 6] NTIS 1964-2007/Apr W2

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[File 34] SciSearch(R) Cited Ref Sci 1990-2007/Apr W1

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[File 434] SciSearch(R) Cited Ref Sci 1974-1989/Dec

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[File 7] Social SciSearch(R) 1972-2007/Apr W1

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[File 15] **ABI/Inform(R)** 1971-2007/Apr 10

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[File 9] Business & Industry(R) Jul/1994-2007/Apr 09

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[File 610] Business Wire 1999-2007/Apr 10

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*File 610: File 610 now contains data from 3/99 forward. Archive data (1986-2/99) is available in File 810.

[File 810] Business Wire 1986-1999/Feb 28

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[File 275] Gale Group Computer DB(TM) 1983-2007/Apr 09

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[File 476] Financial Times Fulltext 1982-2007/Apr 10

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[File 624] McGraw-Hill Publications 1985-2007/Apr 10

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*File 624: Homeland Security & Defense and 9 Platt energy journals added Please see HELP NEWS624 for more

[File 621] Gale Group New Prod.Annou.(R) 1985-2007/Apr 09

(c) 2007 The Gale Group. All rights reserved.

[File 636] Gale Group Newsletter DB(TM) 1987-2007/Apr 06

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[File 613] PR Newswire 1999-2007/Apr 08

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*File 613: File 613 now contains data from 5/99 forward. Archive data (1987-4/99) is available in File 813.

[File 813] PR Newswire 1987-1999/Apr 30

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[File 16] Gale Group PROMT(R) 1990-2007/Apr 09

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[File 160] Gale Group PROMT(R) 1972-1989

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[File 634] San Jose Mercury Jun 1985-2007/Apr 06

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[File 148] Gale Group Trade & Industry DB 1976-2007/Apr 09

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[File 20] Dialog Global Reporter 1997-2007/Apr 10

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[File 2] INSPEC 1898-2007/Apr W1

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[File 35] Dissertation Abs Online 1861-2007/Mar

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[File 65] Inside Conferences 1993-2007/Apr 11

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[File 99] Wilson Appl. Sci & Tech Abs 1983-2007/Mar

(c) 2007 The HW Wilson Co. Allrights reserved.

[File 256] TecInfoSource 82-2007/Oct

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[File 474] New York Times Abs 1969-2007/Apr 12

(c) 2007 The New York Times. All rights reserved.

[File 475] Wall Street Journal Abs 1973-2007/Apr 12

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[File 583] Gale Group Globalbase(TM) 1986-2002/Dec 13

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*File 583: This file is no longer updating as of 12-13-2002.

[File 23] CSA Technology Research Database 1963-2007/Mar

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[File 139] EconLit 1969-2007/Mar

(c) 2007 American Economic Association. All rights reserved.

[File 56] Computer and Information Systems Abstracts 1966-2007/Mar

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[File 344] Chinese Patents Abs Jan 1985-2006/Jan

(c) 2006 European Patent Office. Allrights reserved.

[File 347] **JAPIO** Dec 1976-2006/Dec(Updated 070403)

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[File 350] **Derwent WPIX** 1963-2006/UD=200723

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*File 350: DWPI has been enhanced to extend content and functionality of the database. For more info, visit http://www.diabg.com/dwpi/.

[File 371] French Patents 1961-2002/BOPI 200209

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S4
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OR CRUISE()LINE OR SHIP? ?))
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S11
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S12
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                S S8 AND S4
S13
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                S S12 NOT S10
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                S S13 NOT PY>2001
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                S S14 NOT S11
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                S S16 NOT (S11 OR S15)
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                S S8 AND S7
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                S S18 NOT PY>2001
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? t/3,k/all

11/3,K/1 (Item 1 from file:35) Links

Dissertation Abs Online

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850580 ORDER NO: AAD84-17554

WORKFLOW INTERRUPTIONS AND HUMAN RESOURCE MANAGEMENT IN UNDERGROUND COAL MINING: AN EXPLORATORY STUDY

Author: FIGLER, ROBERT ALBERT

Degree: PH.D. Year: 1984

Corporate Source/Institution: WEST VIRGINIA UNIVERSITY (0256)

Source: Volume 4505A of Dissertations Abstracts International.

PAGE 1505 . 142 PAGES

...serve to increase or diminish coal production outcomes.

The major objectives of this research were **identification** of incidents producing work**interruptions**; and an assessment of the effect of such interruptions on the production a given....workflow, lack of work motivation and poor effort levels on the part of miners donot appear to be at issue: miners **prefer** to run coal relative to other **activities**. However, under conditions of work interruptions, motivation and effort levels may become an issue between...

11/3,K/2 (Item 1 from file: 350) Links

Derwent WPIX

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0009571193 *Drawing available* WPI Acc no: 1999-518114/199943

Related WPI Acc No: 1998-130105; 1998-609652

XRPX Acc No: N1999-385307

Address processing circuit in dynamic random access memory (DRAM)

Patent Assignee: MICRON TECHNOLOGY INC (MICR-N)

Inventor: CASPER S L; PARKINSON W

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
US 5949737	A	19990907	US 1995536005	Α	19950929	199943	В
			US 1997848340	A	19970430		
			US 1998116767	A	19980716		

Priority Applications (no., kind,date): US 1997848340 A 19970430; US 1995536005 A 19950929; US 1998116767 A 19980716

Patent Details

Patent Number	Kind Lan Pgs Draw			Draw	Filing No	Filing Notes			
US 5949737	A	EN	20	11	Continuation of application	US 1995536005			
					Continuation of application	US 1997848340			

Alerting Abstract ... NOVELTY - A delay circuit (16) is controlled by a transition detect signal from a transition detector to output a bar RAS signal without delaying, for a predetermined time, when an address transition is detected. A delayed bar RAS signal is output when an address transition is detected.

...the input (15) to an address bus detects an address transition and outputs a transition detect signal (17) to the delay circuit. A row address decoder has an address input (30) to receive the address from ...

Original Publication Data by Authority

Original Abstracts:

to the decoder, the latch, and the detector. If the monitor detects a transition of the row address, the delay circuit delays the enabling of the row of memory cels and the storing of the row address at least...

Claims:

• •	having a control input coupled to receive the transition detect signal from the transition detectdhe delay circuit being operable to couple the address strobe to an output terminal apredetermined time after a transition of the address to couple the address strobe to the outputterminal without waiting the predetermined time in the event the transition detect signal has not been applied to the control input of the delaycircuit; and an address decoder having

11/3,K/3 (Item 2 from file: 350) Links

Derwent WPIX

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0008631798 Drawing available WPI Acc no: 1998-168712/199815 XRPX Acc No: N1998-134007

Power management method for PC - involves detecting power mode of system and granting device control of

bus before completion of system activities

Patent Assignee: COMPAQ COMPUTER CORP (COPQ)

Inventor: COLLINS M J; DESCHEPPER T J; EDWARDS J R; LARSON J E; REIF J R

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
US 5721935	Α	19980224	US 1995580027	Α	19951220	199815	В
			US 1997801200	A ⁻	19970218		

Priority Applications (no., kind,date): US 1995580027 A 19951220; US 1997801200 A 19970218

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes			
US 5721935	Α	EN	25	11	Continuation of application	US 1995580027		

Alerting Abstract ...power mode. It is determined whether a bus device is granted control of the buswithout requesting control of the bus. Afterpredetermined computer system activities are completed it is detected whether the system is in the first power mode. The...

Original Publication Data by Authority

Claims:

requesting control of the bus;a delay generator coupled to said detector and analyzefor waiting for predetermined computer system activities to complete after it is detected that the computer system is in the first power mode and the bus device is granted control of.... power mode after said predetermined computer system activities have completed and if the bus device is still granted control of the bus without requesting control of the bus and the computer system is still.

11/3,K/4 (Item 3 from file:350) Links

Derwent, WPIX

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0006199527 Drawing available WPI Acc no: 1992-201932/199225 XRPX Acc No: N1992-152815

Displacement detection e.g. detecting unauthorised meter interference - sensing change of state of magnetic elements, with microprocessor connected to elements via direct and inverted amplifiers

Patent Assignee: SCHLUMBERGER IND SA (SLMB)

Inventor: NIVEN R; SIUTA M

Patent Family (5 patents, 12 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 490710	Al	19920617	EP 1991402799	Α	19911022	199225	В
FR 2670285	Al	19920612	FR 199015333	A	19901207	199232	E
EP 490710	B1	19950315	EP 1991402799	A	19911022	199515	E
DE 69108201	E	19950420	DE 69108201	A	19911022	199521	E
			EP 1991402799	A	19911022		
ES 2069245	T3	19950501	EP 1991402799	A	19911022	199524	E

Priority Applications (no., kind,date): FR 199015333 A 19901207

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes		
EP 490710	A1	FR	7	3		<u> </u>	
Regional Designated States,Original	AT BE (CH DE I	K E	S FR C	B GR IT LI NL SE		
EP 490710	B1	FR	7	3			
Regional Designated States,Original	AT BE (CH DE I)K E	S FR C	GB GR IT LI NL SE		
DE 69108201	E	DE			Application	EP 1991402799	
					Based on OPI patent	EP 490710	
ES 2069245	T3	ES			Application	EP 1991402799	
					Based on OPI patent	EP 490710	

Displacement detection e.g. detecting unauthorised meter interference -

Alerting Abstract ... ADVANTAGE - Even temporary displacement is detectable, retrospectively. Operation is unaffected by supplyinterruptions.

Original Publication Data by Authority

Claims:

of the magnetic circuit not being detected following the demagnetization of the circt, or on the contrary being detected at another instant.

11/3,K/5 (Item 4 from file: 350) **Links**

Derwent WPIX

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0005545501 Drawing available
WPI Acc no: 1991-150050/199121
Related WPI Acc No: 1991-149658
XRAM Acc no: C1991-064868
XRPX Acc No: N1991-115220

Automatic cops change - monitors yarn movements to phase actions of yarn bonding and cops change mechanisms

Patent Assignee: SCHLAFHORST & CO AG W (SCHF); SCHLAFHORST & CO W (SCHF)

Inventor: ENGELHARDT D; GRECKSCH H; HAASEN R; RUETH G; RUTH G

Patent Family (6 patents, 5 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 427990	Α	19910522	EP 1990120575	Α	19901026	199121	В
US 5082194	A	19920121	US 1990613088	A	19901114	199206	E
DE 4032617	A	19920416	DE 4032617	Α	19901015	199217	E
EP 427990	A3	19921021	EP 1990120575	A	19901026	199341	E
EP 427990	B 1	19941221	EP 1990120575	A	19901026	199504	E
DE 59008080	G	19950202	DE 59008080	A	19901026	199510	E
			EP 1990120575	A	19901026		

Priority Applications (no., kind,date): DE 3937824 A 19891114; DE 4032617 A 19901015

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing N	Notes
EP 427990	Α	EN				
Regional Designated States,Original	ве сн	DE FR	IT L			
DE 4032617	A	DE	15			
EP 427990	A3	EN				
EP 427990	B1	DE	20	5		
Regional Designated States,Original	ве сн	DE FR	IT L			
DE 59008080	G	DE			Application	EP 1990120575
					Based on OPI patent	EP 427990

Original Titles:

Method and apparatus for evaluating the interruption of winding on a textile winding machine Equivalent Alerting Abstract ... Appts for evaluating the interruption of winding on a textile winding machine

following a yarn break comprises a lower yarn...

Original Publication Data by Authority

...

Original Abstracts:

absence of engagement of a lower yam end after the predetermined number of lower yarnend engaging cycles. Once a lower yarn end is detected, an upper yarn end engaging means performs a predetermined number of attempts to engage... ... end is not detected after a predetermined number of attempts, a problem dicating member is activated to indicate to an operator that normal winding of yarn cannot be restored.

11/3,K/6 (Item 5 from file: 350) **Links**

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0003223246

WPI Acc no: 1984-038502/198407 XRAM Acc no: C1984-016142 XRPX Acc No: N1984-029208

Controlling delivery of gobs to glassware forming machine - using programmable controller and timing pulse

generator

Patent Assignee: EMHART IND INC (EMHA)

Inventor: JAPENGA R J; LARSON J P

Patent Family (7 patents, 6 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 100239	Α	19840208	EP 1983304328	Α	19830727	198407	В
AU 198316847	A	19840202				198412	E
US 4453963	A	19840612	US 1982403245	A	19820729	198426	E
JP 59097538	A	19840605	JP 1983139304	A	19830729	198428	E
EP 100239	В	19880914	EP 1983304328	A	19830727	198837	E
DE 3377982	G	19881020				198843	E
JP 1988065611	В	19881216				198903	E

Priority Applications (no., kind,date): US 1982403245 A 19820729

Patent Details

Patent Number	Kind	Lan	Pgs	Draw Fili	ng Notes
EP 100239	A	EN	36	8	
Regional Designated States, Orginal	DE FR GB	IT			
EP 100239	В	EN			
Regional Designated States, Original	DE FR GB	IT			

Alerting Abstract ... to determine whether it has moved to either a delivery or intercept position in predetermined time. If this hasnot occurred then the gob distributor is automatically retracted...

Original Publication Data by Authority

Original Abstracts:

determined whether this delivery enablepulse occurs within a predetermined time window. In the eventhat either the

leading or the trailing edge of this pulse occurs outside the predetermined time windows, the gob interceptor (14...... pulse occurs within a predetermined time window. In the event either the leading or trailing dge of this pulse occur outside predetermined time windows, the gob interceptor associated with the machine is activated to reject the...

Claims:

responsive to said first transducer means, said actuator signal mnitoring means and said first time delay means for determining, if said second signal occurs after said first predetermined timedelay period after the occurrence...

11/3,K/7 (Item 6 from file: 350) Links

Derwent WPIX

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0003057229

WPI Acc no: 1984-147802/198424

Pacemaker sensing electrical events in atrium and ventricle - paces heart to avoid pacemaker sustained tachycardia and controls ventricle stimulation ratew.r.t. sustained high intrinsic atrial rate

Patent Assignee: INTERMEDICS INC (INTE-N)

Inventor: BAKER R G

Patent Family (5 patents, 11 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 110612 A 19840613	EP 1983306952	Α	19831114	198424	В		
		EP 1987201091	A	19821115			
CA 1230931	Α	19871229				198804	E
EP 110612 B 198802	19880210	EP 1983306952	A	19831114	198806	NCE	
			EP 1987201091	A	19821115		
US 4712556	Α	19871215	US 1982443559	Α	19821122	198806	E
			US 1985780702	A	19850925		
DE 3375643	G	19880317				198812	E

Priority Applications (no., kind,date): US 1982443559 A 19821122; US 1985780702 A 19850925

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
EP 110612	A	EN	81	17	
Regional Designated States, Original	AT BE CH DE	FR GB IT LI NI	SE		
CA 1230931	A	EN			
EP 110612	В	EN			
Regional Designated States, Original	AT BE CH DE	FR GB IT LI NI	. SE		

Alerting Abstract...paced. When an electrical event is sensed in the atrium, the ventricle is paced if no ventricular event is sensed within a predetermined AC delay following the sensing of the event in the atrium...

Original Publication Data by Authority

Claims:

paced. When an electrical event is sensed in the atrium, the ventricle is pacedfino ventricular event is sensed within a

	·		

11/3,K/8 (Item 7 from file: 350) **Links**

Derwent WPIX

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0002600066

WPI Acc no: 1982-P6021E/198245

Service line call interruption circuit - uses speech recognition circuit connected to service line to sense stored

speech signal response

Patent Assignee: MITEL CORP (MTLC)

Inventor: MATTHEWS T H

Patent Family (10 patents, 6 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
DE 3149292	A	19821104	DE 3149292	A	19811212	198245	В
GB 2098830	A	19821124	GB 198419191	A	19840727	198247	E
FR 2504332	A	19821022				198248	E
JP 58012467	A	19830124				198309	E
DE 3149292	С	19831215	DE 3149292	A	19811212	198351	E
CA 1171945	Α	19840731	CA 375725	A	19810416	198435	E
US 4481384	A	19841106	US 1981285654	A	19810721	198447	E
GB 2144944	A	19850313	GB 198130679	Α	19811012	198511	E
GB 2098830	В	19850904				198536	E
GB 2144944	В	19850911				198537	E

Priority Applications (no., kind,date): CA 375725 A 19810416

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
DE 3149292	A	DE	43		
CA 1171945	A	EN			

Alerting Abstract ... connected to the serviceline, an additional switching circuit acquires the numbers diaded and the interruption is instigated when the speech recognition circuit establishes a blocked digit sequence associated with a stored speech signal is dialled.

Equivalent Alerting Abstract ...dialed are passed over the trunk. In the event that the password or voice doesot match the predetermined password or voice, and in the event a predetermined one or group of digits are dialed which match a prohibited digit or group of...

Original Publication Data by Authority

Original Abstracts:

are passed over the trunk. However in the event that the password or voice doe**not** match the **predetermined** password or voice, and in the**event** a **predetermined** one or group of digits are diabd which match a prohibited digit or group of...

Claims:

trunk following said enunciated word signal, and (d) meas for splitting said trunk in the event of both the detection of predetermined ones of said dialed digits and the absence of recognition of said predetermined audio signal.

? t/3,k/all

15/3,K/1 (Item 1 from file:35) **Links**

Dissertation Abs Online

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01143193 ORDER NO: AAD91-02689

DEVELOPMENT OF CRITERIA FOR WARRANTS OF PASSING RELIEF LANES ON TWO-LANE TWO-WAY HIGHWAYS (HIGHWAYS)

Author: JAIN, MUKESH KUMAR

Degree: PH.D. Year: 1990

Corporate Source/Institution: MICHIGAN STATE UNIVERSITY (0128)

Source: Volume 5109B of Dissertations Abstracts International.

PAGE 4497 . 217 PAGES

...length between crests of vertical curves. If a large portion of a road consists of no-passing zones, motorists may violate the established passing restriction thereby increasing the probability of an accident. The use of passing lanes can... ...study the operational benefit gained by providing passing lanes on two-lane highways. Two parameters delay and percentage vehicles in platoon were selected to study the operational benefits due to passing lanes. Similation runs... ... and roadway conditions were plotted against different ADT values. These values were also used to determine the sensitivity of delay to different parameters. The construction cost for passing lane(s) for different termiwere plotted...

15/3,K/2 (Item 1 from file:23) <u>Links</u> CSA Technology Research Database (c) 2007 CSA. All rights reserved.

0004398182 IP Accession No: N93-13253

National Aero-Space Plane: Key issues facing the program. Testimony before the Subcommittee on Technology and Competitiveness, Committee on Science, Space, and Technology, House of Representatives

General Accounting Office, Washington, DC.

Publication Date: 1992

Conference:

, UNITED STATES

Document Type: Report **Record Type:** Abstract **Language:** ENGLISH

Report No: GAO/T-NSIAD-92-26

File Segment: Aerospace & High Technology

Abstract:

...potential cost increases and budget constraints are likely to be significant contributors to future schedule delays in the NASP Program. Furthermore, determining how much funding is needed and when, focusing development activities on needed technologies, and developing.....rik. Nevertheless, testing of several critical components and an analysis of the test results are not expected to occur until afterthe scheduled Phase 3 go-ahead decision in September 1993. (Author)

Descriptors: ...Cost estimates; *Cost reduction; *Costs; *Flight tests; *National aerospace plane program; *Propulsion; *Technologies; *X-30vehicle; Delay; Research and development; Schedules

15/3,K/3 (Item 1 from file:350) Links

Derwent WPIX

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0008710316 Drawing available WPI Acc no: 1998-250770/199822 XRPX Acc No: N1998-198007

Repeater unit especially for Ethernet (RTM) - has dedicated activity line interconnected to each repeater units which also include arbitration unit connected to respective activity line

Patent Assignee: CYPRESS SEMICONDUCTOR CORP (CYPR-N)

Inventor: SOMER G B

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
US 5740174	Α	19980414	US 1995552272	Α	19951102	199822	В

Priority Applications (no., kind,date): US 1995552272 A 19951102

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 5740174	Α	EN	14	6	

Alerting Abstract ... A delay unit is coupled to the activitydetector and delays the transmission of the data on the data bus for a predetermined period of time... ... and to the activity monitor and transmits the data on the data bus after the predetermined period of time if no other activity signals are detected by the activity monitor...

Original Publication Data by Authority

Claims:

transmission of said data on said data bus; a delay unit coupled to said ativity detector and configured to delay said transmission of said dataon said data bus for a predetermined period of time; an activity signal monitor configured to monitor said activity lines...... said predetermined period of time if no other activity signals are detected by said activity monitor.>

15/3,K/4 (Item 2 from file: 350) **Links**

Derwent WPIX

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0008670580 Drawing available WPI Acc no: 1998-209256/199819 XRPX Acc No: N1998-166307

Bus arbitration for a small computer system interface - randomly selects a predetermined time delay slot before being subjected to arbitration on predetermined priority basis

Patent Assignee: LSI LOGIC CORP (LSIL-N); SYMBIOS LOGIC INC (SYMB-N)

Inventor: DEMOSS R A; ROBERT

Patent Family (5 patents, 27 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 836143	A2	19980415	EP 1997304216	Α	19970617	199819	В
JP 10207833	A	19980807	JP 1997265383	A	19970930	199842	E
KR 1998025170	Α	19980706	KR 199750701	Α	19970930	199927	E
TW 353168	A	19990221	TW 1997108406	A	19970617	199929	E
US 6016527	A	20000118	US 1996724596	A	19960930	200011	E

Priority Applications (no., kind,date): US 1996724596 A 19960930

Patent Details

Patent Number	Kind	Lan	Pgs	Draw Fil	ing Notes
EP 836143	A2	EN	20	6	
Regional Designated States, Orginal	AL AT BE CH LU LV MC NL	DE DK ES FI FR GB . PT RO SE SI	GR IE	IT LI LT	
JP 10207833	A	JA	19	6	
KR 1998025170	A	KO		6	
TW 353168	A	ZH			

Alerting Abstract ...delay slot (400). The time delay (404) commences when a bus free state (402) is detected. If at any time during the timedelay the bus becomes busy again, due to another device having a shorter time delayinning control of...... is free to compete for control of the bus subject to arbitration (412) on predetermined priority basis. If control is not achieved then another predetermined time delay slot is randomly seeded by the device. Up to seven SCSI devices can...

15/3,K/5 (Item 3 from file: 350) **Links**

Derwent WPIX

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0007681873 Drawing available WPI Acc no: 1996-303429/199631 XRPX Acc No: N1996-255243

Identifier specification method for computer system - by transmitting demand data from one device to another which has desired identifier, via bus and recognising first device

Patent Assignee: IBM CORP (IBMC); INT BUSINESS MACHINES CORP (IBMC)

Inventor: FRAZIER GR; TSAO GY

Patent Family (2 patents, 2 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
JP 7015447	Α	19950117	JP 199453448	A	19940324	199631	В
US 5544333	Α	19960806	US 199361786	A	19930514	199637	E
			US 1995380473	A	19950130		

Priority Applications (no., kind,date): US 199361786 A 19930514; US 1995380473 A 19950130

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes		
JP 7015447	A	JA	9	5			
US 5544333	A	EN	9	5	Continuation of application	US 199361786	

Original Titles:

System for assigning and identifying devices n bus within **predetermined** period of time without requiring host to do the assignment.

Alerting Abstract ... ADVANTAGE - Recognises device on bus without being delayed.

15/3,K/6 (Item 4 from file:350) Links

Derwent WPIX

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0007442239 Drawing available WPI Acc no: 1996-051338/199606 XRPX Acc No: N1996-042981

Internal combustion engine ignition timing controller -utilises microprocessor to variably advance and retard ignition timing according to calculations based on engine and vehicle speed, throttle and gear information

Patent Assignee: GENERAL MOTORS CORP (GENK)

Inventor: BAUERLE P A; CUBR A E; GARRETT D P; MARSH R A; MATHEWS D S; VANEK M J

Patent Family (5 patents, 2 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 690225	A2	19960103	EP 1995201520	A	19950609	199606	В
EP 690225	A3	19960508	EP 1995201520	A	19950609	199628	E
US 5573474	Α	19961112	US 1994267320	A	19940628	199651	E
EP 690225	Bl	19990818	EP 1995201520	A	19950609	199937	E
DE 69511481	E	19990923	DE 69511481	Α	19950609	199945	Е
			EP 1995201520	A	19950609		

Priority Applications (no., kind,date): US 1994267320 A 19940628

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing I	Notes
EP 690225	A2	EN	11	4		
Regional Designated States,Original	DE FR	GB				
EP 690225	A3	EN				
US 5573474	A	EN	9	4		
EP 690225	B1	EN				
Regional Designated States,Original	DE FR	GB				
DE 69511481	E	DE			Application	EP 1995201520
					Based on OPI patent	EP 690225

Original Publication Data by Authority

Original Abstracts:

speed to produce an appropriate engine torque output waveform for optimally cancel the vehicle vibrational

disturbances. The phase shift is accomplished by applying a time deay to the engine speed derivative. A negative... ... engine torque output waveform for optimally canceling the vehicle brational disturbances. The phase shift is accomplished by applying a time delay to the engine speed derivative. A negative bias is added to the...

Claims:

predetermined timing of maximum torque, the bias ignition timing retard correction being initiated a biaselay time after the detected predetermined changein throttle plate position, the bias ignition timing retardorrection having initially abias value sufficient to accommodate the maximum advance in the initialcycle of the sinusoidal ignition timing retard correction without advancing ignition

15/3,K/7 (Item 5 from file: 350) Links

Derwent WPIX

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0006032855 Drawing available WPI Acc no: 1992-268999/199233 XRPX Acc No: N1992-205809

Feasibility analysis method for schedule analysis decision support system - evaluating whether proposed transportation schedule can be met by available vehicles in respect of input data, and outputting associated feasibility value

Patent Assignee: UNIV PENNSYLVANIA (UYPE-N)

Inventor: HARKER P T; JOVANOVIC D

Patent Family (4 patents, 3 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
AU 199180200	A	19920625	AU 199180200	A	19910704	199233	В
CA 2046984	Α	19920619	CA 2046984	A	19910712	199236	E
US 5177684	Α	19930105	US 1990629417	A	19901218	199304	E
AU 644664	В	19931216	AU 199180200	A	19910704	199406	E

Priority Applications (no., kind,date): US 1990629417 A 19901218

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Note	es
AU 199180200	A	EN	97	18		
CA 2046984	A	EN				
US 5177684	A	EN	54	18		
AU 644664	В	EN			Previously issued patent	AU 9180200

Alerting Abstract ...into a computer system and evaluating in accordance with that data whether a proposed transportation schedule can be met without the extra cost due to delay of vehicles at certain points. An output indicative of the evaluation is provided, and gives a measure...

Equivalent Alerting Abstract...SCAN) decision support system to determine the feasibility of the scheduledn the transportation system, vehicles are delayed to avoid conflicts with other vehicles which would otherwise colde, because the vehicles maybe.....ADVANTAGE - Provides optimal vehicleschedules with respect to cost resulting from vehicle delay.

Original Publication Data by Authority

... ∩--:

Original Abstracts:

the feasibility of the schedules is disclosed. In a transportation system, vehicles are delayed to avoid conflicts with other vehicles which would otherwise collide becase the vehicles may be travelling along the same

• • •

Claims:

each conflict resolution point being a meetpoint at which one of said two vehicles care delayed to permit the other of said two vehicles to pass thereby avoiding a colbion between said two..... being identified according to said chronological sequence and taking into consideration a possible delay casid one vehicle in each identified potential conflict, (c) generating an initialmeet-pass plan using a depth-first search bounded by delay costs arising from delaying saidone vehicle at one of said identified conflictresolution points for each potential conflict for an amount of time such that each potential conflict..... substantially equal to an accumulation of all dely costs resulting from said one vehicle being delayed in each potential conflict, said delay cost defining an upper bound; (d) estimating a maximum cost benefit...... substantially equal to an accumulation of alltelay costs resulting from said one vehicle being delayed in each potential conflict, and if said delay cost of said alternative met-pass plan so generated...... upper bound with said delay cost of said alternative met-pass plan; (f) identifying onemeet-pass having a substantially minimaldelay cost among said initial and alternative met-pass plans so generated by comparing each alternative meet-pass..... upper bound; and (g) controlling the movement of said vehicles according to said identified meet-pass plan, said vehicles being delayed at said identified conflictresolution points for the amount of time specified by saididentified meet-pass plan.

? t/3,k/all

17/3,K/1 (Item 1 from file:350) Links

Derwent WPIX

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0007440935 Drawing available WPI Acc no: 1996-049784/199605 XRPX Acc No: N1996-041635

Absorption water chiller/heater - automatically regulates opening upper limiters of throttle valves of fuel combustion burner

Patent Assignee: TOKYO GAS CO LTD (TOLG)

Inventor: EDERA M; KOJIMA H; NAKAMURA M; NAKAMURA M E M; OKA M

Patent Family (15 patents, 20 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1995034789	A1	19951221	WO 1995JP1151	A	19950609	199605	В
JP 7332787	A	19951222	JP 1994128469	A	19940610	199609	E
EP 713062	Al	19960522	EP 1995921137	Α	19950609	199625	E
			WO 1995JP1151	A	19950609		
JP 8152220	A	19960611	ЛР 1994291572	A	19941125	199633	E
JP 8152221	A	19960611	JP 1994291664	Α	19941125	199633	E
JP 8152222	A	19960611	JP 1994291736	A	19941125	199633	E
JP 8152224	A	19960611	JP 1994291845	A	19941125	199633	E
US 5678414	A	19971021	WO 1995JP1151	Α	19950609	199748	E
			US 1996592292	Α	19960409		
CN 1129477	A	19960821	CN 1995190543	A	19950609	199751	E
US 5829260	A	19981103	WO 1995JP1151	A	19950609	199851	E
			US 1996592292	A	19960409		
			US 1997881074	A	19970624	Ì	
US 5865035	A	19990202	WO 1995JP1151	Α	19950609	199912	E
		· "	US 1996592292	Α	19960409		
			US 1997881078	Α	19970624		
US 5878587	A	19990309	WO 1995JP1151	Α	19950609	199917	E
			US 1996592292	A	19960409		
			US 1997881075	A	19970624		
KR 213430	B1	19990802	WO 1995JP1151	Α	19950609	200104	E
			KR 1996700640	Α	19960207		
KR 244110	B1	20000315	WO 1995JP1151	A	19950609	200122	E
			KR 1996700640	A	19960207		
			KR 1999701220	Α	19990212		
CN 1149369	С	20040512	CN 1995190543	A	19950609	200617	Е

Priority Applications (no., kind,date): JP 1994291845 A 19941125; JP 1994291736 A 19941125; JP 1994291664 A 19941125; JP 1994128469 A 19940610; JP 1994291572 A 19941125

Patent Details

				Pater	nt Details		
Patent Number	Kind	Lan	Pgs	Draw	aw Filing Notes		
WO 1995034789	A1	JA	103	62			
National Designated States, Original	CN KF	RUS					
Regional Designated States,Original	AT BE	CH I	DE DI	K ES F	FR GB GR IE IT LU MC N	IL PT SE	
JP 7332787	A	JA	9	17			
EP 713062	A1	EN	79	62	PCT Application	WO 1995JP1151	
					Based on OPI patent	WO 1995034789	
Regional Designated States, Original	ES IT						
JP 8152220	A	JA	17				
JP 8152221	Α	JA	8				
JP 8152222	Α	JA	11				
JP 8152224	A	JA	12				
US 5678414	Α	EN	72	62	PCT Application	WO 1995ЛР1151	
		Ĭ			Based on OPI patent	WO 1995034789	
US 5829260	A	EN			Division of application	WO 1995JP1151	
					Division of application	US 1996592292	
	Î				Division of patent	US 5678414	
US 5865035	A	EN			Division of application	WO 1995JP1151	
					Division of application	US 1996592292	
					Division of patent	US 5678414	
US 5878587	A	EN			Division of application	WO 1995JP1151	
					Division of application	US 1996592292	
					Division of patent	US 5678414	
KR 213430	B1	ко			PCT Application	WO 1995JP1151	
KR 244110	B1	ко			PCT Application	WO 1995JP1151	
					Division of application	KR 1996700640	

Alerting Abstract ... shutdown signal of a solution pump or acombustion burner, a clock judges whether ornot a predetermined period of time has lapsed after the shutdown of the pump or the burner. If...

Original Publication Data by Authority

Original Abstracts:

solution pump or a combustion burner, clock means judges whether or not a predetermined peod of time has lapsed after the shutdown of the solution pump or the combustion burner, and if predetermined..... of crystallization in the exhaust heat exchanger tubing and/or corrosion resulting from an increase in high temperature regenerator temperatures and to otherwise prevent the production of unavailable refrigerant, save energy in an exhaust heat charge mode of operation..... heat exchanger tubing and/or corrosion resulting from an increase in high temperature regenerator temperatures and to otherwise prevent the production of unavailable refrigerant, save energy in an exhaust heat charge mode of operation, and to insure refrigerating ability...... corrosion resulting from an increase in high temperature regenerator temperatures and to otherwise prevent the production of unavailable refrigerant, save energy in an exhaust heat charge mode of operation and to insure refrigerating ability even when exhaust water temperatures..... in high temperature regenerator temperatures and to otherwise prevent the production of unavailable refrigerant, save energy in an exhaust heat charge mode of operation, and to insure refrigerating ability even when exhaust water temperatures are low without reducing the...... pump or the combustion burner, and if predetermined period of time has lapsed, control means outputs a control signal to a branch means to permit a fluid containing exhaust heat to bypass the...

Claims:

of the exhaust-heat utility, said method comprising the steps of: detecting a signal for**interrupting** an operation of a solution pump or a combustion burner; judging whether a predetermined timehas passed from interrupting... t /3, k/all

20/3,K/1 (Item 1 from file:2) Links

Fulltext available through: Institute of Electrical and Electronics Engineers USPTO Full Text Retrieval Options

INSPEC

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05975300 INSPEC Abstract Number: B9508-1265B-003, C9508-5210B-003

Title: Energy models for delay testing

Author Chakradhar, S.T.; Iyer, M.A.; Agrawal, V.D. Author Affiliation: NEC, Princeton, NJ, USA

Journal: IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems vol. 14, no. 6 p.

728-39

Publication Date: June 1995 Country of Publication: USA

CODEN: ITCSDI ISSN: 0278-0070

U.S. Copyright Clearance Center Code: 0278-0070/95/\$04.00

Language: English

Subfile: B C

Copyright 1995, IEE

Abstract: ... of the individual gate energy functions. To derive tests for a given delay fault, this inction is suitably modified such that any minimum-energy state is guaranteed to be a test. The specific modifications to the energy function depend on the type (robust or nonrobust, with or without hazards) of delay test desired.... transitive closure based test generation technique is very effective in generating tests. This approach efficiently determines a delay test or establishes that no test is possible for the given delay fault. We report experimental results on various sequential ...

20/3,K/2 (Item 2 from file:2) Links

Fulltext available through: <u>USPTO Full Text Retrieval Options</u>

INSPEC

(c) 2007 Institution of Electrical Engineers. Allights reserved. 05241394 INSPEC Abstract Number: C9211-5260S-001

Title: Decline in accuracy of automatic speed recognition as a function of time on task: fatigue or voice drift?

Author Frankish, C.; Jones, D.; Hapeshi, K.

Author Affiliation: Dept. of Psychol., Bristol Univ., UK

Journal: International Journal of Man-Machine Studies vol.36, no.6 p. 797-816

Publication Date: June 1992 Country of Publication: UK

CODEN: IJMMBC ISSN: 0020-7373

U.S. Copyright Clearance Center Code: 0020-7373/92/060797+20\$03.00/0

Language: English

Subfile: C

Abstract: ...was to provide some indication of the magnitude and time course of this decline iperformance, and to clarify the nature of underlying changes in speech behaviour. Three experiments are described Experiment 1 confirmed that there is a fall. ...occurs for both naive and practised subjects. In Experiment 2no recovery was observed in recognition performance when short restbreaks were scheduled, indicating that vocal fatigue wasnot a major factor. The effects of template retraining in mid-session were investigated in Experiment...

20/3,K/3 (Item 1 from file:35) Links

Dissertation Abs Online

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01107509 ORDER NO: AAD90-13985

RATIONING BY WAITING IN THE CONTEXT OF ALTERNATIVE MEDICAL CARE PROVIDERS (WAITING LISTS)

Author: OZMINKOWSKI, RONALD JAMES

Degree: PH.D. Year: 1989

Corporate Source/Institution: THE UNIVERSITY OF MICHIGAN (0127)

Source: Volume 5101B of Dissertations Abstracts International.

PAGE 164 . 271 PAGES

...to be imposed by waiting, and (3) the expected delay to receive the good or service. Changes over time along any of these dimensions can motivate consumers to leave a waiting st.....waiting. Specifically, perceived health status, perceptions of the quality of VA inpatient care, whether onot the veteran is scheduled for surgery, and whether veterans have a usual medical provider are the most important determinants of willingness to wait for inpatient VA care. Expected delay to receive treatment is also a significant determinant. Surprisingly, marginal cost factors seem to have very little influence over witing list behavior.

This...

20/3,K/4 (Item 2 from file:35) <u>Links</u>
Dissertation Abs Online
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756335 ORDER NO: AAD81-21201

DISSERTATION PROCRASTINATION

Author: GREEN, GERTRUDE DORSEY

Degree: PH.D. Year: 1981

Corporate Source/Institution: UNIVERSITY OF WASHINGTON (0250)

Source: Volume 4204A of Dissertations Abstracts International.

PAGE 1548 . 195 PAGES

...of procrastination on the chronic and situational dimensions. The Survey of Study Habits and Attitudes--Delay/Avoidance Subscale was used to evaluate academic attitudes and habits. The Personality Research Form and the Adjective Check List were used... ...receiving support; and to value being held in high esteem by acquaintances. Chronic procrastinators didnot like changes in their environments and they preferred to work with others rather than alone. Situational procrastinators valued organization and structure, liked being with people.

20/3, K/5 (Item 3 from file: 35) **Links**

Dissertation Abs Online

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743282 ORDER NO: AAD81-08652

AN EXPLORATION OF COLLABORATION AMONG EDUCATION AGENCIES AS A MEANS OF PROMOTING LOCAL SCHOOL IMPROVEMENTS

Author: SCANNELL, JUNE ALICE POUND

Degree: ED.D. Year: 1980

Corporate Source/Institution: UNIVERSITY OF ILLINOIS AT URBANA-CHAMPAIGN (0090)

Source: Volume 4111A of Dissertations Abstracts International.

PAGE 4569 . 131 PAGES

...specific practices which promote workable partnerships among state agencies, regional units and localistricts, (d) identifies circumstances that interfere with cooperative efforts, and finally, (e) outlines a serie of recommendations for circumventing barriers and....upon able leadership, substantial advantage would be gained in focusing administrator pre-service and inservice training on techniques for developing locally-initiatedchanges, as well as for exploiting the potential of collaborative endeavor. In addition, state agencies might...of the collaborative process that could serve as prototypes. Moreover, inasmuch as collaborative networks datot materialize automatically, state and federal officials should establish some kind of incentive structure which encourages and facilitates various kinds of joint partnerships. Finally...

20/3,K/6 (Item 1 from file: 347) Links

JAPIO

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03346723 **Image available**

METHOD FOR DETECTING INTERFERENCE TO RECEPTION OF RADIO EQUIPMENT FOR CONTROL

Pub. No.: 03-009623 [JP 3009623 A] **Published:** January 17, 1991 (19910117)

Inventor: NARA TOSHIYUKI

ITO NORIYOSHI

Applicant: MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company or Corporation), JP (Japan)

Application No.: 01-143033 [JP 89143033]

Filed: June 07, 1989 (19890607)

Journal: Section: E, Section No. 1049, Vol. 15, No. 121, Pg. 42, March 25, 1991 (19910325)

METHOD FOR DETECTING INTERFERENCE TO RECEPTION OF RADIO EQUIPMENT FOR

CONTROL

ABSTRACT

...connection request signal from a terminal station with alternated radio equipment for speech by automatically alternating the function of radio equipment for control with the radio equipment for speech having another frequency classified by every frequency when reception interference is detected.

... ... an operation is migrated to step 15, and it is decided whether or not an interference detecting flag is turned on. In step 20, information representing whether or not frame synchronization is established is inputted, and in step 21, it is judged whether or not the frame synchronization is established. Thus, it is possible to detect the reception interference at the radio equipment for control based on carrier detection and the establishment/non-establishment

20/3,K/7 (Item 1 from file:350) **Links**

Derwent WPIX

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0011167404 Drawing available WPI Acc no: 2002-104947/200214 XRPX Acc No: N2002-077989

FCC-certifiable range-gated radar apparatus for automatic door opener, mixes each reflected microwave pulse with its delayed local oscillator pulse, to generate baseband signal

Patent Assignee: INTERLOGIX INC (INTE-N)

Inventor: BARROWS C R; BIGELOW S K; MCCOY S J; MCDONALD K B

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
US 6239736	B1	20010529	US 1999130580	P	19990421	200214	В
			US 1999430254	Α	19991029		

Priority Applications (no., kind,date): US 1999130580 P 19990421; US 1999430254 A 19991029

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
US 6239736	Bl	EN	16	10	Related to Provisional	US 1999130580

...and receives reflected pulses from the object. Each reflected pulse is mixed with its associated delayed local oscillator pulse, by homodyne peak detector (36), to generate a baseband signal indicating presence and movement of object.

Original Publication Data by Authority

Original Abstracts:

RGR detector senses the presence of moving human sized objects whin predetermined ranges. Movingobjects beyond the ranges are not sensed. The RGR detector employs a pulsed microwave oscillator (12) that is triggered by a system clock (14) and... ... is determined by the delay imposed between the transmitted pulses, the first being a transmitted pulse and the second being a local oscillator pulse. Each received 5.8 GHzpulse is mixed down to... ... is adjustable as a function of range and maybe set to discriminate object sizes. The PIR detector operates conventionally, but its output is logically combined with the RGRetector output to reduce false...

20/3,K/8 (Item 2 from file:350) Links

Derwent WPIX

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0008841043 Drawing available WPI Acc no: 1998-387465/199833 XRPX Acc No: N1998-302193

Cordless controller for dedicated printer connected to PC,WP - has timer which defines predetermined interval beyond which alternative computer controls printer, if host computer becomes inactive

Patent Assignee: ITI INNOVATIVE TECHNOLOGY LTD (ITII-N)

Inventor: HABER A P; KAHN S M

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
US 5774637	A	19980630	US 1993150815	A	19931112	199833	В

Priority Applications (no., kind,date): US 1993150815 A 19931112

Patent Details

Patent Number	Kind	Lan	Pgs	Draw Filing Notes
US 5774637	A	EN	24	11

Original Publication Data by Authority

Original Abstracts:

an infrared transmission link by which a alternate/portable computer can detect a break in the normal printing mode and temporarily access the dedicated printer for printing tasks. The receiver/switch unit is.....computer to gain access to the printer only if during a predetermied interval, the dedicated printer is not busy with a printing task already assigned to it. Once the portable computer has gained temporaryaccess, the time...

Claims:

enable shared, cordless printing on the dedicated printern accordance with printing task directed by an alternate computer not linked to the dedicated printer by a hard-wie, said method comprising the steps of generating...

20/3,K/9 (Item 3 from file:350) **Links**

Derwent WPIX

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0007919253 Drawing available WPI Acc no: 1997-006997/199701 XRPX Acc No: N1997-006404

Cover synchronising linkage e.g. for book binding machine - includes controller to drive coverfeeding device when detection delay between both sensors is not within preset range

Patent Assignee: SK SALES KK (SKSA-N)

Inventor: SAWAZA T

Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
JP 8276682	Α	19961022	JP 1995101690	Α	19950403	199701	В

Priority Applications (no., kind,date): JP 1995101690 A 19950403

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
JP 8276682	Α	JA	7	5	

includes controller to drive coverfeeding device when detection delay between both sensors is not within preset range

Alerting Abstract ... A specific time delay is defined between the detecting times of these two sensors. When the pulse count corresponding to the observed time delay between the detection times of these sensors does not correspond to preset range, the controller drives the cover feeddevice... ... ADVANTAGE - Improves adjustment accuracy and work efficiency. Raises processing speed.

20/3,K/10 (Item 4 from file: 350) **Links**

Derwent WPIX

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0007304791 Drawing available WPI Acc no: 1995-365997/199547 XRPX Acc No: N1995-270831

VLSI microprocessor computer aided design method - involves storing netlist defining circuit block connectivity in memory and determining timing delay for path to meet timing criteria

Patent Assignee: ROSS TECHNOLOGY INC (ROSS-N)

Inventor: CARMEAN D; MUNDKUR Y

Patent Family (1 patents, 1 countries)

Patent Number	atent Number Kind Date		Application Number	Kind	Date	Update	Туре
US 5459673	A	19951017	US 1990605121	A	19901029	199547	В
			US 1992900516	A	19920617		
			US 1993122132	A	19930914		
			US 1994225492	A	19940411		

Priority Applications (no., kind,date): US 1993122132 A 19930914; US 1992900516 A 19920617; US 1990605121 A 19901029; US 1994225492 A 19940411

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes			
US 5459673	Α	EN	15	6	Continuation of application	US 1990605121		
					Continuation of application	US 1992900516		
					Continuation of application	US 1993122132		

involves storing netlist defining circuit block connectivity in memory and determining timing delay for path to meet timing criteria

Alerting Abstract ...or more paths required to meet set performance criteriatotal capacitance at path nodes is determined, along with its time delay.

Original Publication Data by Authority

Claims:

performance criteria determining total capacitance at each of the nodes of the path; determining the tieming delay for the path, for each of the one or more blocks currently driving one of the nodes...... for the path while using the current

1	block; if the timing delay is not within the predetermined performancecriteria, searching the library for an appropriate
,	block, if the thining delay 3 not whith the predetermined performance criteria, searching the notary for an appropriate
(cell that represents a different block which performs the same function and has performance characteristics which
•	would improve circuit performance; if the appropriate cell is found, substituting the different block for the current
ı	block; and generating an updated version of the netlist which reflects

.

20/3,K/11 (Item 5 from file:350) Links

Derwent WPIX

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0006085989 Drawing available WPI Acc no: 1992-325071/199240 XRPX Acc No: N1992-248499

Alternate processor continuation of failed processor task - by storing predetermined register contents from failed processor in predetermined locations for use by healthy processor

Patent Assignee: IBM DEUT GMBH (IBMC); INT BUSINESS MACHINES CORP (IBMC)

Inventor: SUTTON A J

Patent Family (5 patents, 7 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 505706	A1	19920930	EP 1992102134	A	19920208	199240	В
WO 1992017841	A1	19921015	WO 1992EP383	Α	19920224	199244	Е
US 5214652	A	19930525	US 1991675393	A	19910326	199322	E
EP 505706	B1	19970514	EP 1992102134	A	19920208	199724	E
DE 69219657	E	19970619	DE 69219657	A	19920208	199730	E
			EP 1992102134	A	19920208		

Priority Applications (no., kind,date): US 1991675393 A 19910326

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing N	Notes
EP 505706	A1	EN	19	10		
Regional Designated States,Original	DE FR	GB				
WO 1992017841	A1	EN	39	10		
National Designated States,Original	CS DE	HU PL	RU			
US 5214652	Α	EN	17	10		
EP 505706	B1	EN	25	10		
Regional Designated States,Original	DE FR	GB				
DE 69219657	Е	DE			Application	EP 1992102134
					Based on OPI patent	EP 505706

 $\textbf{Alternate processor continuation of failed processor } \textbf{task-} \dots \\$

Original Titles:

Alternate processor continuation of task of failed processor.....Alternate processor continuation of the task of a

failed processor......Alternate processor continuation oftask of failed processor.....ALTERNATE PROCESSOR CONTINUATION OF TASK OF FAILED PROCESSOR

Alerting Abstract ... The processor (CPUh) receiving the identification signal is interrupted and a processor (CPUh) selected to continue execution of the program. The stored program continuation...

Equivalent Alerting Abstract ... contents of registers in a failing processor into storage to store a predetermined program continuation interruption state when the processor detects a hard error condition. A signal identifying the failing processor is sent to an other...

Original Publication Data by Authority

Original Abstracts:

Claims:

in the failing processor (CPUf)into storage to store a predetermined program continuation interruption state when the processor detects a hardware failure condition; sending (33) a signal identifying the failing processor (CPUf) to at least one... ... a healthy processor (CPUh) from said storage the stored program execution program or programtask continuation interruption state of the failing processor to continue execution of the program from a lat successfully executed instruction without having any... ... other processor of validity of contents stored by the copying step; and signalling healthy processor() in the system of a request for a healthy processor to continue execution of the program or program task if...

20/3,K/12 (Item 6 from file: 350) Links

Derwent WPIX

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0005315361 Drawing available WPI Acc no: 1990-312568/199042 XRPX Acc No: N1991-108159

Optical communications network for binary information - has each peripheral providing identification during configuration phase checked with returned bit pattern

Patent Assignee: ALCATEL ALSTHOM CIE GEN ELECTRICITE (COGE); ALCATEL AUSTRIA AG (@GE);

ALCATEL NV (COGE)

Inventor: KNEIDINGER M; STRASSER H; WOLFGANG J

Patent Family (8 patents, 14 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
AT 198902446	Α	19880915	AT 19892446	A	19891024	199042	В
EP 425470	Α	19910502	EP 1990890273	Α	19901009	199118	E
JP 3151742	A	19910627	JP 1990286950	Α	19901024	199132	E
US 5107361	Α	19920421	US 1990602469	A	19901024	199219	E
EP 425470	A3	19921014	EP 1990890273	A	19901009	199340	E
EP 425470	B1	19970514	EP 1990890273	A	19901009	199724	E
DE 59010715	G	19970619	DE 59010715	A	19901009	199730	E
			EP 1990890273	A	19901009		
ES 2103269	T3	19970916	EP 1990890273	Α	19901009	199744	E

Priority Applications (no., kind,date): AT 19892446 A 19891024

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing :	Notes
AT 198902446	Α .	DE	-	3		
EP 425470	A	EN				
Regional Designated States,Original	ве сн	DE ES F	R GI	3 GR I	T LI LU NL SE	•
US 5107361	A	EN	8			
EP 425470	A3	EN				
EP 425470	B1	DE	11	3		
Regional Designated States,Original	BE CH	DE DK I	ES FI	R GB (GR IT LI LU NL SE	
DE 59010715	G	DE			Application	EP 1990890273
					Based on OPI patent	EP 425470
ES 2103269	Т3	ES			Application	EP 1990890273
					Based on OPI patent	EP 425470

Alerting Abstract ...control a logic (17) initiating a search repetition. The transmitter levels adjustable, its timedelay determined by measuring the time lag between transmission of the identification and reception of the returned... Equivalent Alerting Abstract ... ADVANTAGE - Integration of terminals at random instants into opical transmission network without knowledge of predetermined address being necessary. Maintains most favourable conditions of communication at given time.

Original Publication Data by Authority

Claims:

of the terminal station being adjusted as a function of the path attenuation between terminal attenuation and central station, and the frame format comprising a number of time slots for circuit-switched information and...

20/3,K/13 (Item 7 from file: 350) **Links**

Derwent WPIX

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0004715102

WPI Acc no: 1989-078568/198911

Object collision detection apparatus - generates volumes swept by moving objects for consideration as solid bodies and determines shortest distance between bodies

Patent Assignee: IBM CORP (IBMC); INT BUSINESS MACHINES CORP (IBMC)

Inventor: SHIMADA K

Patent Family (5 patents, 6 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 307091	Α	19890315	EP 1988307445	Α	19880811	198911	В
US 4888707	Α	19891219	US 1988233686	A	19880818	199008	E
CA 1282174	C	19910326				199117	E
EP 307091	B1	19940302	EP 1988307445	Α	19880811	199409	E
DE 3888055	G	19940407	DE 3888055	A	19880811	199415	E
	Ī		EP 1988307445	A	19880811		

Priority Applications (no., kind,date): JP 1987224232 A 19870909

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing I	Notes
EP 307091	A	EN	17	8		
Regional Designated States,Original	DE FR	GB IT				
US 4888707	Α	EN	14			
CA 1282174	С	EN				
EP 307091	B 1	EN	18	8		
Regional Designated States,Original	DE FR	GB IT				
DE 3888055	G	DE			Application	EP 1988307445
					Based on OPI patent	EP 307091

Alerting Abstract ...dimensional space. For the case where two objects (A,B)more independenty of one another, changing their rentation, for possiblefunction statuses are defined. These corresp to a statusA, collision is detected at time t =te......If it is determined that there is interference at t =te then status D is signalled. Ifnot, lmax (ts,te,A) and lmax (ts,te, B) are calculated. If a determining condition for collision is not established then a status C is signalled. Further calculations are effected to determine status B or...

Original Publication Data by Authority

Claims:

dimensional space. For the case where two objects (A,B) more independently of one another changing their rentation, for possible function statuses are defined. These corresp to a status A, collision is detected at time t = te..... If it is determined that there is interference at t = te then status D is signalled. If not, lmax (ts,te,A) and lmax (ts,te, B) are calculated. If a determining condition for collision is not established then a status C is signalled. Further calculations are effected to determine status B or..

20/3,K/14 (Item 8 from file:350) Links

Derwent WPIX

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0003469876

WPI Acc no: 1985-243989/198540

Fuel injection control appts. for IC engine - periodically samples throttle aperture for determining deceleration, and determines correcting quantity for delay in control

Patent Assignee: HITACHI LTD (HITA)

Inventor: KASHIWAYA M; MORITA K; SAKAMOTO M

Patent Family (3 patents, 10 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 156358	Α	19851002	EP 1985103566	Α	19850326	198540	В
US 4644923	A	19870224	US 1985716638	A	19850327	198710	E
CA 1241092	Α	19880823				198838	E

Priority Applications (no., kind,date): JP 198459133 A 19840327

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
EP 156358	A	EN	19	5	
Regional Designated States, Original	CH DE FR	GB IT LI NL SE			
CA 1241092	Α	EN			

periodically samples throttle aperture for determining deceleration, and determines correcting quantity for delay in control

Alerting Abstract ...throttle aperture, for determining deceleration. Upon each determination of deceleration, a correcting quantity is accumulatively determined so that deceleration is corrected for compensating delay in the control of fuel injection, in dependence on the magnitude of throttle change...

Equivalent Alerting Abstract...aid of the integrated deceleration correcting coefficients when the value resulting from the comparison is not smaller than a predetermined value...

Original Publication Data by Authority

Original Abstracts:

determining deceleration. Upon everydetermination of the deceleration, a correcting quantity is accumulatively determined so **that** deceleration is corrected for compensating delay in the control of fuel injection in dependence on magnitude of change in the throttle aperture. An inproved engine performance and optimum air-fuel ratio control can

be accomplished...... Upon every determination of the deceleration, a correting quantity is accumulatively determined so that deceleration corrected for compensating delay in the control of fuel injection in dependence on magnitude of change in the throttleaperture. An improved engine performance and optimum air-fuel ratio control can be accomplished.

Claims:

â

throttle aperture, for determining deceleration. Upon each determination of deceleration, a correcting quantity is accumulatively **determined** so that deceleration is corrected for compensating **lelay** in the control of fuel injection, in dependence on the magnitude of throttle change.

20/3,K/15 (Item 9 from file:350) Links

Derwent WPIX

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0002934899

WPI Acc no: 1984-012658/198403

Automatically variable delay function for typewriter leyboard - adjusts duration for which key must be depressed to initiate repetitive character display or printing as function of typing speed

Patent Assignee: IBM CORP(IBMC)
Inventor: JOHNSON C F; WILLIAMS J M

Patent Family (7 patents, 7 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Туре
EP 97816	Α	19840111	EP 1983105166	Α	19830525	198403	В
AU 198313853	Α	19840105				198408	E
BR 198303291	A	19840207				198413	E
US 4490055	A	19841225	US 1982393928	A	19820630	198502	E
CA 1199300	A	19860114				198607	E
EP 97816	В	19880518	EP 1983105166	A	19830525	198820	E
DE 3376636	G	19880623				198826	E

Priority Applications (no., kind,date): US 1982393928 A 19820630

Patent Details

	r atom Dotans					
Patent Number	Kind	Lan	Pgs	Draw	Filing 1	Notes
EP 97816	A	EN	29	3		
Regional Designated States, Original	DE FR GB	IT				
BR 198303291	A	PT				7
CA 1199300	A	EN				
EP 97816	В	EN				
Regional Designated States, Orginal	DE FR GB	IT		 		

Original Titles:

Automatically adjusted delay function for timed repeat character capability of a keyboard.....Automatically adjusted delay function for timed repeat character capability of akeyboard.....Automatically adjustable delay function for timed typamatic

Original Publication Data by Authority

Original Abstracts:

Automatically adjusted delayfunction for timed repeat character capability of akeyboard.

A typewriter (10) is described which has an electronic keyboard (12) utilizing a timed delay to determine whether a depressed key is indicative of the desire to print or display repetitive letters represented by that key. The typewriter... ... held depressed and when that time approaches but does not exceed the preset automatic delay time, the delay time is then extended a predetermined amount.... keyboard utilizing a timed delayto determine whether a depressed key is indicative of the desire to print or display repetitive letters represented by that key. The typewriter iprovided with a technique... ... time approaches but does not exceed the preset automatic delay time, the delaytime is then extended a predetermined amount.

show files

[File 15] ABI/Inform(R) 1971-2007/Apr 11

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[File 16] Gale Group PROMT(R) 1990-2007/Apr 11

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[File 148] Gale Group Trade & Industry DB 1976-2007/Apr 11

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[File 160] Gale Group PROMT(R) 1972-1989

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[File 275] Gale Group Computer DB(TM) 1983-2007/Apr 11

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[File 621] Gale Group New Prod.Annou.(R) 1985-2007/Apr 11

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[File 9] Business & Industry(R) Jul/1994-2007/Apr 11

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[File 476] Financial Times Fulltext 1982-2007/Apr 12

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[File 610] Business Wire 1999-2007/Apr 12

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*File 610: File 610 now contains data from 3/99 forward. Archive data (1986-2/99) is available in File 810.

[File 613] PR Newswire 1999-2007/Apr 08

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*File 613: File 613 now contains data from 5/99 forward. Archive data (1987-4/99) is available in File 813.

[File 624] McGraw-Hill Publications 1985-2007/Apr 11

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*File 624: Homeland Security & Defense and 9 Plat energy journals added Please see HELP NEWS624 for more

[File 634] San Jose Mercury Jun 1985-2007/Apr 10

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[File 636] Gale Group Newsletter DB(TM) 1987-2007/Apr 11

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[File 810] Business Wire 1986-1999/Feb 28

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[File 813] PR Newswire 1987-1999/Apr 30

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[File 625] American Banker Publications 1981-2007/Apr 10

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[File 268] Banking Info Source 1981-2007/Apr W1

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[File 626] Bond Buyer Full Text 1981-2007/Apr 11

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[File 267] Finance & Banking Newsletters 2007/Apr 02

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[File 348] EUROPEAN PATENTS 1978-2007/ 200714

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[File 349] PCT FULLTEXT 1979-2007/UB=20070405UT=20070329

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*File 349: For important information about IPCR/8 and forthcoming changes to the IC= index, see HELP NEWSIPCR.

; ds Set Items Description S (WITHOUT OR WITH()OUT OR "NOT" OR NO OR EXCLUD??? OR 1491277 OMITT???) (7N) (SCHEDULE? ? OR PREDEFIN? ? OR PRESET OR DEFIN? ? OR DESIGNATE? ? OR PREDESIGNATE? ? OR PREDETERMINE? ? OR ESTABLISH?? OR PREESTABLISH? OR PREFER? ? OR PREFERR? OR PRE()(DEFIN? ? OR SET OR DETERMIN? ? OR ESTABLISH? ?) OR UNSCHEDULE? ?) 242357 S (DETECT? OR IDENTIF? OR RECOGNI? OR DETERMIN? OR EVALUAT? OR FIND?) (7N) (INTERRUPT? OR INTERFER? OR DELAY? OR BREAK? ?) 1014835 S (SCHEDULE? ? OR PREDEFIN? ? OR PRESET OR DEFIN? ? OR DESIGNATE? ? OR PREDESIGNATE? ? OR PREDETERMINE? ? OR ESTABLISH?? OR PREESTABLISH? OR PREFER? ? OR PREFERR? OR PRE() (DEFIN? ? OR SET OR DETERMIN? ? OR ESTABLISH? ?) OR UNSCHEDULE? ?) (7N) (ACTIVITIES OR EVENT? ? OR APPOINTMENT? ? OR MEETING? ? OR ENGAGEMENT? ? OR CONFERENCE?) S (S3 OR (DELAY?? OR CANCEL?) (3N) (TRAIN OR FLIGHT? ? OR BUS OR VEHICLE? ? 1117441

OR CRUISE()LINE OR SHIP? ?)) 2395523 S (INCREASE OR RAISE OR BOOST? OR MAXIMI?) (7N) (DEAL??? OR TRAD??? OR PURCHAS??? OR EXCHANG??? OR SELL??? OR SALE? ?) 24494 S S5(7N) (DETECT? OR IDENTIF? OR RECOGNI? OR DETERMIN? OR EVALUAT? OR S6 FIND?) S7 2376912 S (ALTERNAT??? OR SUBSTITUT??? OR ADJUST? OR CHANG? OR MODIF?) (7N) (SERVICE? ? OR SUPPORT? ? OR ASSIST??? OR ASSISTANCE OR PERFORMANCE OR FUNCTION? ? OR JOB? ? OR TASK? ? OR WORK? ?) 793 S S1(7N)S2 S9 S S8(7N)S3 24 S10 8 S S9 NOT PY>2001 S11 25 S S8(3N)S4 1 S S11 NOT S9 S12 S S8(7N)S6 S13 0 S14 15 S S8(7N)S7 S15 6 S S14 NOT PY>2001

? t/3,k/all

10/3K/1 (Item 1 from file: 348) Links

EUROPEAN PATENTS

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00804280

Method and system for supporting interactive text correction and user guidance features

Verfahren und System zur Unterstutzung von Textkorrektur und Benutzerfuhrung Methode et systeme de support de correction de textes et assistance utilisateur

Patent Assignee:

• MICROSOFT CORPORATION; (749863)

One Microsoft Way; Redmond, Washington 98052-6399 (US) (Applicant designated States: all)

Inventor:

Gipson, Dale L.

5216,240th Avenue, N.E.; Redmond, Washington 98053; (US)

Legal Representative:

Meddle, Alan Leonard (33761)

FORRESTER & BOEHMERT Franz-Joseph-Strasse 38; 80801 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	747837	A2	19961211	(Basic)
	EP	747837	A3	19990825	
Application	EP	96304014		19960603	
Priorities	US	486407		19950606	

Designated States:

DE; FR; GB;

International Patent Class (V7): G06F-017/27Abstract Word Count: 108

NOTE: 1

NOTE: Figure number on first page: 1

Туре	Pub. Date	Kind	Text

Publication: English Procedural: English Application English

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1259

SPEC A	(English)	EPAB96	13506
Total Word Count (Document A) 14765		,	_
Total Word Count (Document B) 0			
Total Word Count (All Documents) 14765			

Specification: ...evaluation of parts of a composite rule to a later event interval where a later event can be detected. A sequence rule is scheduled with a delay such that it is not evaluated until a specified number of event interval changes occur. For example, if an event in...

10/3K/2 (Item 2 from file: 348) **Links**

EUROPEAN PATENTS

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00515439

Multiprocessor system.

Multiprozessorsystem.

Systeme multiprocesseur.

Patent Assignee:

• International Business Machines Corporation; (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US) (applicant designated states: DE;FR;GB;IT)

Inventor:

· Fukuda, Minehiro

3-7-6 Sagamiohono; Sagamihara-shi, Kanagawaken; (JP)

Oba, Nobuyuki

3-13-26-404 Sugekitaura, Tama-ku; Kawasaki-shi, Kanagawa-ken; (JP)

Nakada, Takeo

3 ban 3 gou, Iihara-cho; Kawaguchi-shi, Saiama-ken; (JP)

Legal Representative:

• Blakemore, Frederick Norman (28381)

IBM United Kingdom Limited Intellectual Property Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	511476	A2	19921104	(Basic)
	EP	511476	A3	19930623	
Application	EP	92104061		19920310	
Priorities	JP	92116694		19910422	

Designated States:

DE; FR; GB; IT;

International Patent Class (V7): G06F-013/364; G06F-009/46; G06F-015/80; Abstract Word Count: 104

Count

Туре	Pub. Date	Kind		Text
Publication: English				
Procedural: English				
Application English				
	Available Text	Language	Update	Word

CLAIMS A	(English)	616
SPEC A	(English)	3347
Total Word Count (Document A) 3963		
Total Word Count (Document B) 0		
Total Word Count (All Documents) 3963		

Claims: ...data indicating a second interrupt priority and for changing said second priority every time a predetermined event occurs; means for determining whether or not an interrupt request be accepted on the basis of said first interrupt priority; and means for determining...

10/3K/3 (Item 3 from file: 348) Links

EUROPEAN PATENTS

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00401209

Apparatus and method for coupling a data processor to alien information handling apparatus

Anordung und Verfahren zum Verbinden eines Datenprozessors mit einem unbekannten

Informationsverarbeitungssystem

Appareil et procede pour connecter un processeur de donnees avec un systeme tranger du traitment des domees

Patent Assignee:

• International Business Machines Corporation (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US) (applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

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• Joyce, James Maurice

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• Loffredo, John Mario

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• Sanderson, Kenneth Russell

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Legal Representative:

• Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited IntellectualProperty Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	400841	A2	19901205	(Basic)
	EP	400841	A3	19940202	
	EP	400841	B1	19980902	
Application	EP	90305311		19900516	
Priorities	US	353114		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT;

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; , Abstract Word Count: 219

Туре	Pub. Date	Kind	Text
Publication: English			

Procedural: English Application English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9836	764
CLAIMS B	(German)	9836	656
CLAIMS B	(French)	9836	844
SPEC B	(English)	9836	71127
Total Word Count (Document A) 0			
Total Word Count (Document B) 73391			
Total Word Count (All Documents) 73391			

10/3K/4 (Item 4 from file: 348) Links

EUROPEAN PATENTS

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00401208

Servicing interrupts in a data processing system

Unterbrechungsbedienung in einem Datenverarbeitungssystem

Prise en charge d'interruptions dans un systeme de traitemnt de données

Patent Assignee:

• International Business Machines Corporation, (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US)

(applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;ILI;NL;SE)

Inventor:

Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

· Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

· Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

• Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

Legal Representative:

Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited IntellectualProperty Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398696	A2	19901122	(Basic)
	EP	398696	A3	19940105	
	EP	398696	B1	19970723	
Application	EP	90305309		19900516	
Priorities	US	353117		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT;

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; G06F-013/26; Abstract Word Count: 214

Tr	Dub Data	TZ!	Trave
l Tyne	Pub. Date	Kind	l lext
- 7 100	1 45. 54.6	7 1111 (4	10

Publication: English

Procedural: English Application English

Available Text	Language	Update	Word Count
CLAIMS A	(English)		700
SPEC A	(English)		70506
CLAIMS B	(English)	9707W4	715
CLAIMS B	(German)	9707W4	619
CLAIMS B	(French)	9707W4	829
SPEC B	(English)	9707W4	70530
Total Word Count (Document A) 71213			
Total Word Count (Document B) 72693			
Total Word Count (All Documents) 143906			

Specification: ... condition prior to effecting an information transfer. The unit inhibits the information transfer in the **event** a fault is **detected**. The module, however, can continue operation -without interruption or delay - and effect the information transfer from the non-inhibited partner unit.

Other units...

10/3K/5 (Item 5 from file: 348) **Links**

EUROPEAN PATENTS

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00401207

A single physical main storage unit shared by two or more processors executing respective operating systems

Physischer, einziger Hauptspeicher, anteilig genutzt durch zwei oder mehr Prozesoren, die ihr jeweiliges Betriebssystem ausfuhren

Memoire principalephysiquement unique, partagee par deux ou plusieurs processeurs executant leurs systemes operationnels respectifs

Patent Assignee:

• International Business Machines Corporation (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US)

(applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

• Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

• Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

• Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

• Baker, Ernest Dysart

12032 Deer Run; Raleigh North Carolina, 27614; (US)

• Suarez, Gustavo Armando

21482 Woodchuck Lane; Boca Raton, FL 33428; (US)

Legal Representative:

• Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited Intellectua Property Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398695	A2	19901122	(Basic)
	EP	398695	A3	19940202	
	EP	398695	B1	19980902	
Application	EP	90305308		19900516	
Priorities	US	353113		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; G06F-009/46; Abstract Word Count: 219

Туре	Pub. Date	Kind	Text
Publication: English		-	

Procedural: English Application English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9836	678
CLAIMS B	(German)	9836	583
CLAIMS B	(French)	9836	795
SPEC B	(English)	9836	70889
Total Word Count (Document A) 0			
Total Word Count (Document B) 72945			
Total Word Count (All Documents) 72945			

10/3K/6 (Item 6 from file: 348) Links

EUROPEAN PATENTS

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00401206

Fault tolerant data processing system

Fehlertolerantes Datenverarbeitungssystem

Systeme de traitement de donnees a tolerance de fautes

Patent Assignee:

• International Business Machines Corporation (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US)

(applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

Baker, Ernest Dysart

12032 Deer Run; Raleigh, North Carolina 27614; (US)

o Dinwiddie, John Monroe, Jr.

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• Grice, Lonnie Edward

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Joyce, James Maurice

1544 N.W. 9th Street; Boca Raton, FL 33486; (US)

• Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

Suarez, Gustavo Armando

21482 Woodchuck Lane; Boca Raton, FL 33428; (US)

Legal Representative:

• Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited IntellectualProperty Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398694	A2	19901122	(Basic)
	EP	398694	A3	19940202	1
	EP	398694	B1	19980909	
Application	EP	90305307		19900516	
Priorities	US	353116		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE;

International Patent Class (V7): G06F-011/16; G06F-009/44; G06F-015/16; Abstract Word Count: 219

Туре	Pub. Date	Kind	·Text
Publication: English			-

Publication: English Procedural: English Application: English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9837	610
CLAIMS B	(German)	9837	572
CLAIMS B	(French)	9837	714
SPEC B	(English)	9837	71492
Total Word Count (Document A) 0		•	
Total Word Count (Document B) 73388			
Total Word Count (All Documents) 73388			

10/3K/7 (Item 7 from file: 348) Links

EUROPEAN PATENTS

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00401205

Method and apparatus for adding a data processing function to a data processing system

Verfahren und Anordnung zum Hinzufugen von einer Datenverarbeitungsfunkon zu einem

Datenverarbeitungssystem

Methode et appareil pour l'addition d'un fonction de traitement des donnees a un systeme de traitement de donnees

Patent Assignee:

• International Business Machines Corporation (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US) (applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

• Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

• Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

• Joyce, James Maurice

1544 N.W. 9th Street; Boca Raton, FL 33486; (US)

• Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

• Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

• Baker, Ernest Dysart

12032 Deer Run; North Carolina, 27614; (US)

Legal Representative:

Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited IntellectualProperty Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398693	A2	19901122	(Basic)
	EP	398693	A3	19940202	
	EP	398693	B1	19980909	
Application	EP	90305306		19900516	
Priorities	US	353111		19890517	

Designated States:

AT, BE, CH, DE, DK, ES, FR, GB, GR, IT,

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; G06F-013/12; Abstract Word Count: 219

Type	Pub. Date	Kind	Text
Publication: English		,	

Procedural: English Application English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9837	1109
CLAIMS B	(German)	9837	979
CLAIMS B	(French)	9837	1299
SPEC B	(English)	9837	71715
Total Word Count (Document A) 0			
Total Word Count (Document B) 75102			
Total Word Count (All Documents) 75102			

10/3K/8 (Item 1 from file:349) Links
PCT FULLTEXT
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00443927
A COMMUNICATION SYSTEM ARCHITECTURE
ARCHITECTURE D'UN SYSTEME DE COMMUNICATION

Patent Applicant/Patent Assignee:

MCI WORLDCOM INC;
;;

EASTEP Guido M;
;;

LITZENBERGER Paul R;

;;

• OREBAUGH Shannon R;

;;

• ELLIOTT Isaac K;

, ,

• STELLE Rick;

, ,

• SCHRAGE Bruce;

, ,

• BAXTER Craig A;

, ,

• ATKINSON Wesley;

· ·

• KNOSTMAN Chuck;

; ;

• CHEN Bing;

, ,

VANDERSLUIS Kristan;

	Country	Number	Kind	Date
Patent	WO	9834391	A2	19980806
Application	WO	98US1868		19980203
Priorities	US	97794555		19970203
	US	97794114		19970203
	US	97794689		19970203
	US	97807130		19970210
	US	97798208		19970210
	US	97795270		19970210
	US	97797964		19970210
	US	97800243		19970210
	US	97798350		19970210
	US	97797445		19970210
	US	97797360		19970210

Designated States: (All protection types applied unless otherwise stated - for applications 2004+)

Publication Language English

Filing Language:

Fulltext word count: 156226

Detailed Description:

...the Public Switched Network (PSTN) 1960 via a gateway 1950. The gateway 1950 in apreferred embodiment provides a virtual connection from a circuit switched cli in the PSTN 1960 and...

? t/3,k/all

12/3K/1 (Item 1 from file: 348) Links

EUROPEAN PATENTS

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00301781

A system and method for automatically controlling a vehicle speed to a desired cruise speed with a release function

System und Verfahren zur automatischen Steuerung einer Fahrzeuggeschwindigkeit auf eine geschwunschte Reisegeschwindigkeit mit Abschaltfunktion

Systeme et methode pour commander automatiquement la vitesse d'un vehicule par rapport a une vitesse de croisiere desiree avec mecanisme de mise hors circuit

Patent Assignee:

• **NISSAN MOTOR CO., LTD.**; (228490)

2 Takara-cho, Kanagawa-ku; Yokohama-shi Kanagawa-ken; (JP) (Proprietor designated states: all)

Inventor:

Yamamoto, Isao

4688-1, Sobudai 3-chome; Zama-shi Kanagawa-ken; (JP)

Inoue, Hiroshi

5951-49, Okazaki; Hiratsuka-shi Kanagawa-ken; (JP)

• Mori, Kazuyuki

36-1-328, Sagamiohno 7-chome; Sagamihara-shi Kanagawa-ken; (JP)

Suzuki, Koichi

36-1-924, Sagamiohno 7-chome; Sagamihara-shi Kanagawa-ken; (JP)

• Nakano, Kinichiro

930-2, Aiko; Atsugi-shi Kanagawa-ken; (JP)

Nomura, Hiroyuki

3-805, Kugenumahigashi 2-chome, Fujisawa-shi Kanagawa-ken, (JP)

• Yoshida, Kiyoshi

12-7, Shonandai 1-chome; Fujisawa-shi Kanagawa-ken; (JP)

• Etoh, Yoshiyuki

21, Yaguchidai Naka-ku; Yokohama-shi Kanagawa-ken; (JP)

Legal Representative:

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	Country	Number	Kind	Date	
Patent	EP	315207	A2	19890510	(Basic)

	EP	315207	A3	19890913	
	EP	315207	B1	19931027	
	EP	315207	В2	20000405	
Application	EP	88118447		19881104	
Priorities	JР	87279133		19871106	

Designated States:

Type

Publication: English

Total Word Count (Document B) 6533
Total Word Count (All Documents) 6533

DE; GB;

International Patent Class (V7): B60K-031/10Abstract Word Count: 141

Pub. Date

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200014	1010
CLAIMS B	(German)	200014	949
CLAIMS B	(French)	200014	1188
SPEC B	(English)	200014	3386

Claims: ... speed falls in a predetermined speed range in which the operation of step (c) iperformed adequately; and

Kind

Text

(e) cancelling said operation of step (c) and instructing first interrupt means to interrupt power supply to the engine driving force adjusting mechanismif the detected vehicle speed does not fall in said predetermined speed range,

characterized in that a second step of determining whether said detected vehicle speed falls in said predetermined speed range is performed independently of the operation of...

15/3,K/1 (Item 1 from file:15) <u>Links</u>
ABI/Inform(R)
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30971136 96-20529

Are you ready for the coming storm?

Crooks, John W
Communications v32n1 pp: 44
Jan 1995

ISSN: 0010-356X Journal Code: CMN

Word Count: 899

Text:

...place to supervise the construction of a new communications site. The radio technician is usually not trained to detect proper construction techniques, time schedules, valid delays, or scope of work changes. Thus, the enterprise task has been delegated incorrectly due to outside pressures rather than by proper classification.

Enterprise tasks...

15/3K/2 (Item 1 from file: 348) Links

EUROPEAN PATENTS

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00401209

Apparatus and method for coupling a data processor to alien information handling apparatus

Anordung und Verfahren zum Verbinden eines Datenprozessors mit einem unbekannten

In formations ver arbeitungs system

Appareil et procede pour connecter un processeur de donnees avec un systeme tranger du traitment des domees

Patent Assignee:

• International Business Machines Corporation, (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US)

(applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

• Baker, Ernest Dysart

12032 Deer Run Raleigh; North Carolina27614; (US)

Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

• Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

Joyce, James Maurice

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Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

• Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

Legal Representative:

• Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited Intellectua Property Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	400841	A2	19901205	(Basic)
	EP	400841	A3	19940202	
	EP	400841	B1 .	19980902	
Application	EP	90305311		19900516	
Priorities	US	353114		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT;

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; ; Abstract Word Count: 219

Туре	Pub. Date	Kind	Text
D. 1.11			

Publication: English Procedural: English Application: English

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9836	764
CLAIMS B	(German)	9836	656
CLAIMS B	(French)	9836	844
SPEC B	(English)	9836	71127
Total Word Count (Document A) 0			
Total Word Count (Document B) 73391	 		
Total Word Count (All Documents) 73391			

15/3K/3 (Item 2 from file: 348) Links

EUROPEAN PATENTS

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00401208

Servicing interrupts in a data processing system

Unterbrechungsbedienung in einem Datenverarbeitungssystem

Prise en charge d'interruptions dans un systeme de traitemet de donnees

Patent Assignee:

International Business Machines Corporation, (200120)

Old Orchard Road; Armonk, N.Y. 10504; (US)

(applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

• Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

Legal Representative:

• Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited Intellectua Property Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398696	A2	19901122	(Basic)
	EP	398696	A3	19940105	
	EP	398696	B 1	19970723	
Application	EP	90305309		19900516	
Priorities	US	353117		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT;

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; G06F-013/26; Abstract Word Count: 214

1 700	TO 1 TO 4	17. h	an .
l Type i	Pub. Date	Kina	PYT
1 1 JPC	I ub. Dute	ILIIIG	ICAL

Publication: English

Procedural: English Application English

. Available Text	Language	Update	Word Count
CLAIMS A	(English)		700
SPEC A	(English)		70506
CLAIMS B	(English)	9707W4	715
CLAIMS B	(German)	9707W4	619
CLAIMS B	(French)	9707W4	829
SPEC B	(English)	9707W4	70530
Total Word Count (Document A) 71213	-		
Total Word Count (Document B) 72693			
Total Word Count (All Documents) 143906			

Specification: ...the entire module to continue operating. A user is seldom aware of such a fault**detection** and transition to off-line status, except for the display or other presentation of a maintenance request **service** the off-line unit. The card arrangement albws easy removal and replacement.

The memory unit...

15/3K/4 (Item 3 from file: 348) Links

EUROPEAN PATENTS

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00401205

Method and apparatus for adding a data processing function to a data processing system

Verfahren und Anordnung zum Hinzufugen von einer Datenverarbeitungsfunkon zu einem

Datenverarbeitungssystem

Methode et appareil pour l'addition d'un fonction de traitement des donnees a un systeme de traitement de donnees

Patent Assignee:

• International Business Machines Corporation (200120)

Old Orchard Road, Armonk, N.Y. 10504; (US) (applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;IU;NL;SE)

Inventor:

Dinwiddie, John Monroe, Jr.

112 Pacer Circle; West Palm Beach, FL 33414; (US)

• Grice, Lonnie Edward

252 N.W. 44th Street; Boca Raton, FL 33431; (US)

• Joyce, James Maurice

1544 N.W. 9th Street; Boca Raton, FL 33486; (US)

• Loffredo, John Mario

2694 S.W. 14th Drive; Deerfield Beach, FL 33442; (US)

• Sanderson, Kenneth Russell

1132 Widgeon Road; West Palm Beach, FL 33414; (US)

• Baker, Ernest Dysart

12032 Deer Run; North Carolina, 27614; (US)

Legal Representative:

Bailey, Geoffrey Alan (27921)

IBM United Kingdom Limited Intellectua Property Department Hursley Park; Winchester Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	398693	A2	19901122	(Basic)
	EP	398693	A3	19940202	
	EP	398693	B1	19980909	
Application	EP	90305306		19900516	
Priorities	US	353111		19890517	

Designated States:

AT; BE; CH; DE; DK; ES; FR; GB; GR; IT;

LI; LU; NL; SE;

International Patent Class (V7): G06F-015/16; G06F-013/12; Abstract Word Count: 219

Туре	Pub. Date	Kind	Text
Publication: English			
Procedural: English			
Application:English			

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9837	1109
CLAIMS B	(German)	9837	979
CLAIMS B	(French)	9837	1299
SPEC B	(English)	9837	71715
Total Word Count (Document A) 0			
Total Word Count (Document B) 75102			
Total Word Count (All Documents) 75102		•	

15/3K/5 (Item 4 from file: 348) Links

EUROPEAN PATENTS

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00301781

A system and method for automatically controlling a vehicle speed to a desired cruise speed with a release function

System und Verfahren zur automatischen Steuerung einer Fahrzeuggeschwindigkeit auf eine geschwunschte Reisegeschwindigkeit mit Abschaltfunktion

Systeme et methode pour commander automatiquement la vitesse d'un vehicule par rapport a une vitesse de croisiere desiree avec mecanisme de mise hors circuit

Patent Assignee:

• **NISSAN MOTOR CO., LTD.**; (228490)

2 Takara-cho, Kanagawa-ku; Yokohama-shi Kanagawa-ken; (JP) (Proprietor designated states: all)

Inventor:

• Yamamoto, Isao

4688-1, Sobudai 3-chome; Zama-shi Kanagawa-ken; (JP)

• Inoue, Hiroshi

5951-49, Okazaki; Hiratsuka-shi Kanagawa-ken; (JP)

• Mori, Kazuyuki

36-1-328, Sagamiohno 7-chome; Sagamihara-shi Kanagawa-ken; (JP)

• Suzuki, Koichi

36-1-924, Sagamiohno 7-chome, Sagamihara-shi Kanagawa-ken, (JP)

• Nakano, Kinichiro

930-2, Aiko; Atsugi-shi Kanagawa-ken; (JP)

• Nomura, Hirovuki

3-805, Kugenumahigashi 2-chome; Fujisawa-shi Kanagawa-ken; (JP)

• Yoshida, Kiyoshi

12-7, Shonandai 1-chome; Fujisawa-shi Kanagawa-ken; (JP)

• Etoh, Yoshiyuki

21, Yaguchidai Naka-ku; Yokohama-shi Kanagawa-ken; (JP)

Legal Representative:

• TER MEER STEINMEISTER & PARTNER GbR (100061)

Mauerkircherstrasse 45; 81679 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	315207	A2	19890510	(Basic)
	EP	315207	A3	19890913	

	EP	315207	B 1	19931027	
	EP	315207	B2	20000405	
Application	EP	88118447		19881104	
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CLAIMS B	(English)	200014	1010
CLAIMS B	(German)	200014	949
CLAIMS B	(French)	200014	1188
SPEC B	(English)	200014	3386
Total Word Count (Document A) 0			
Total Word Count (Document B) 6533			
Total Word Count (All Documents) 6533			

Claims: ...of the control unit (30) falls in apredetermined field range in which the controlunit (30) works adequately, and is connected to the interrupt means (41,42,43) which is adapted to interrupt the power supply of the engine driving force adjusting mechanism (31) f the determined electric field does not fall...

15/3K/6 (Item 1 from file:349) <u>Links</u>
PCT FULLTEXT
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STEREOLITHOGRAPHIC BEAM PROFILING

PROFILAGE DE FAISCEAU STEREOLITHOGRAPHIQUE

Patent Applicant/Patent Assignee:

3D SYSTEMS INC;

Country Number Kind Date WO Patent 8911085 A1 19891116 WO 89US1559 Application 19890417 US Priorities 88830 19880418 US 88816 19881108 US 88837 19881108 88907 US 19881108 US 88801 19881108

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